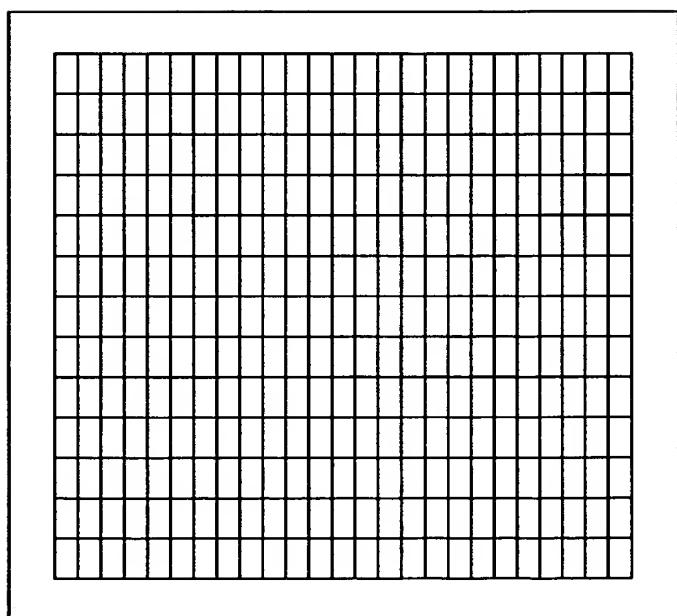


After Saw

FIG. 1B



6.6 " Square Silicon Substrate

FIG. 1A

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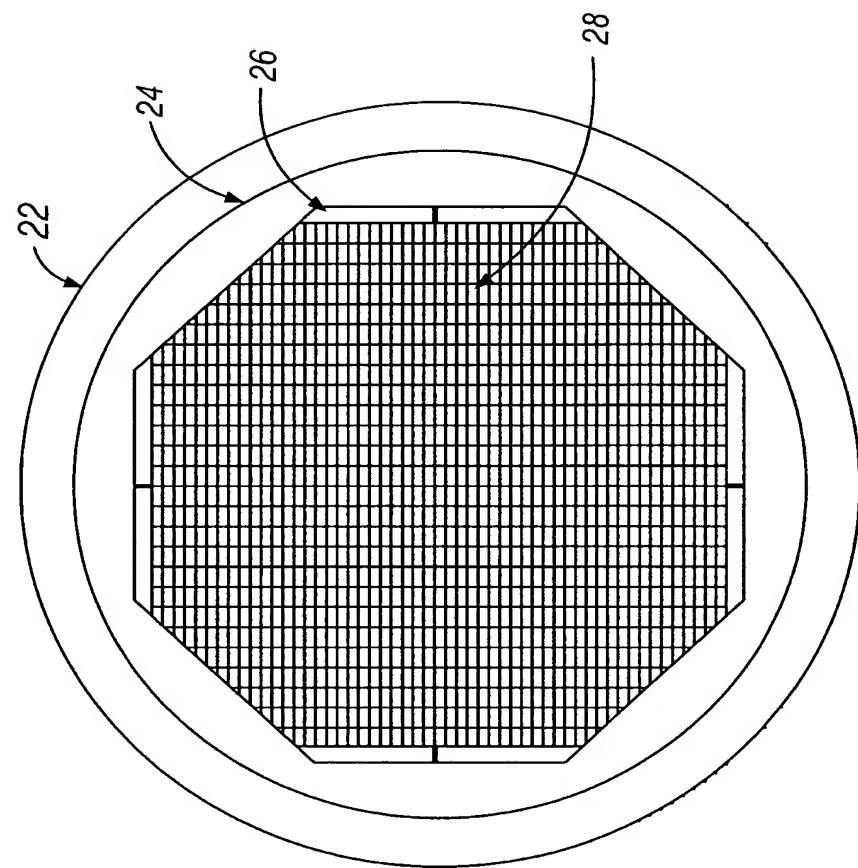


FIG. 2B

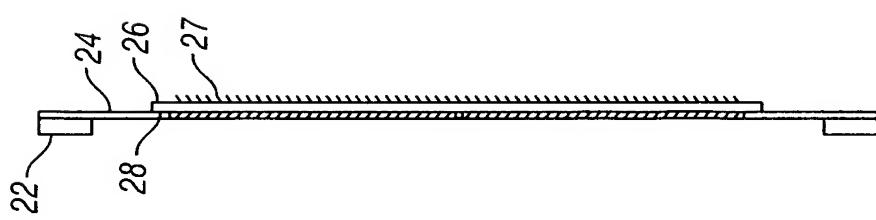


FIG. 2A

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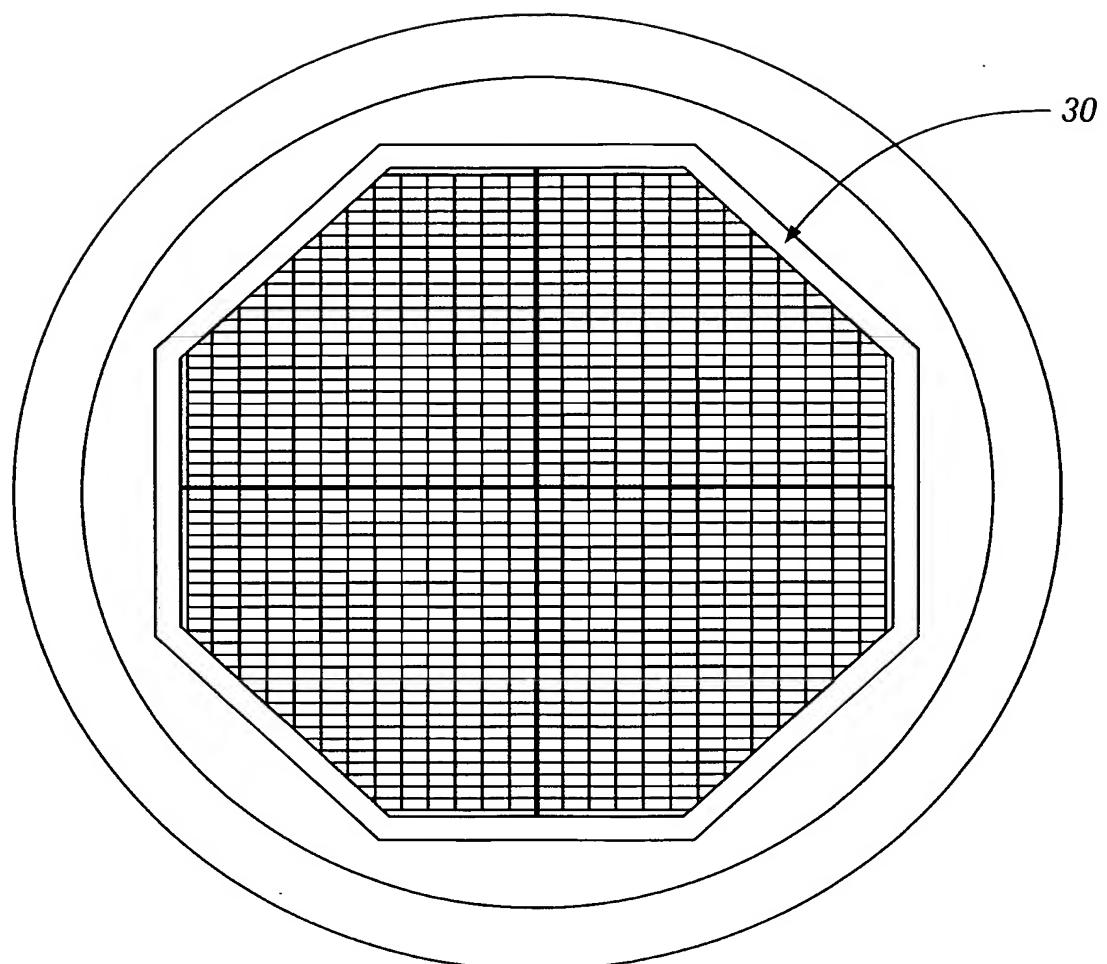
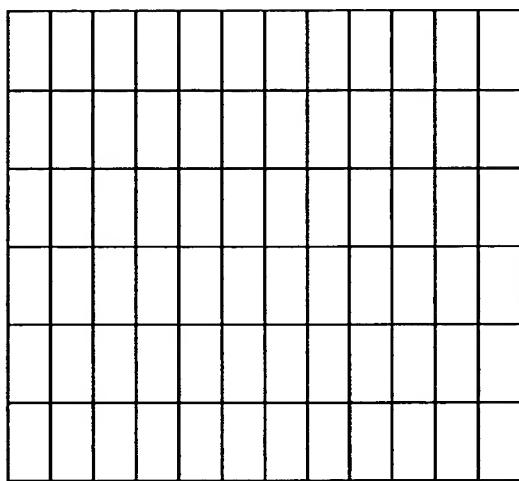


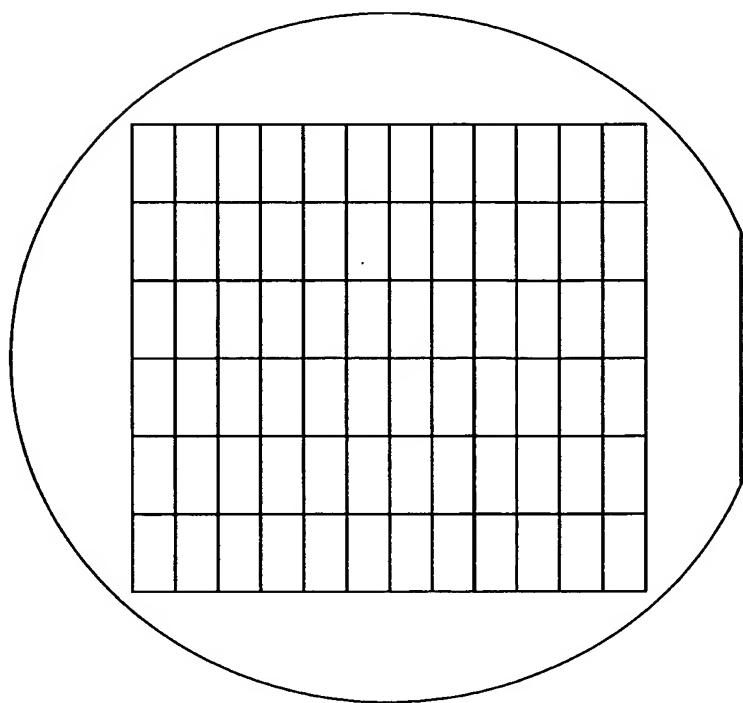
FIG. 3

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Sawed Substrate

FIG. 4B



4" Ceramic Wafer

FIG. 4A

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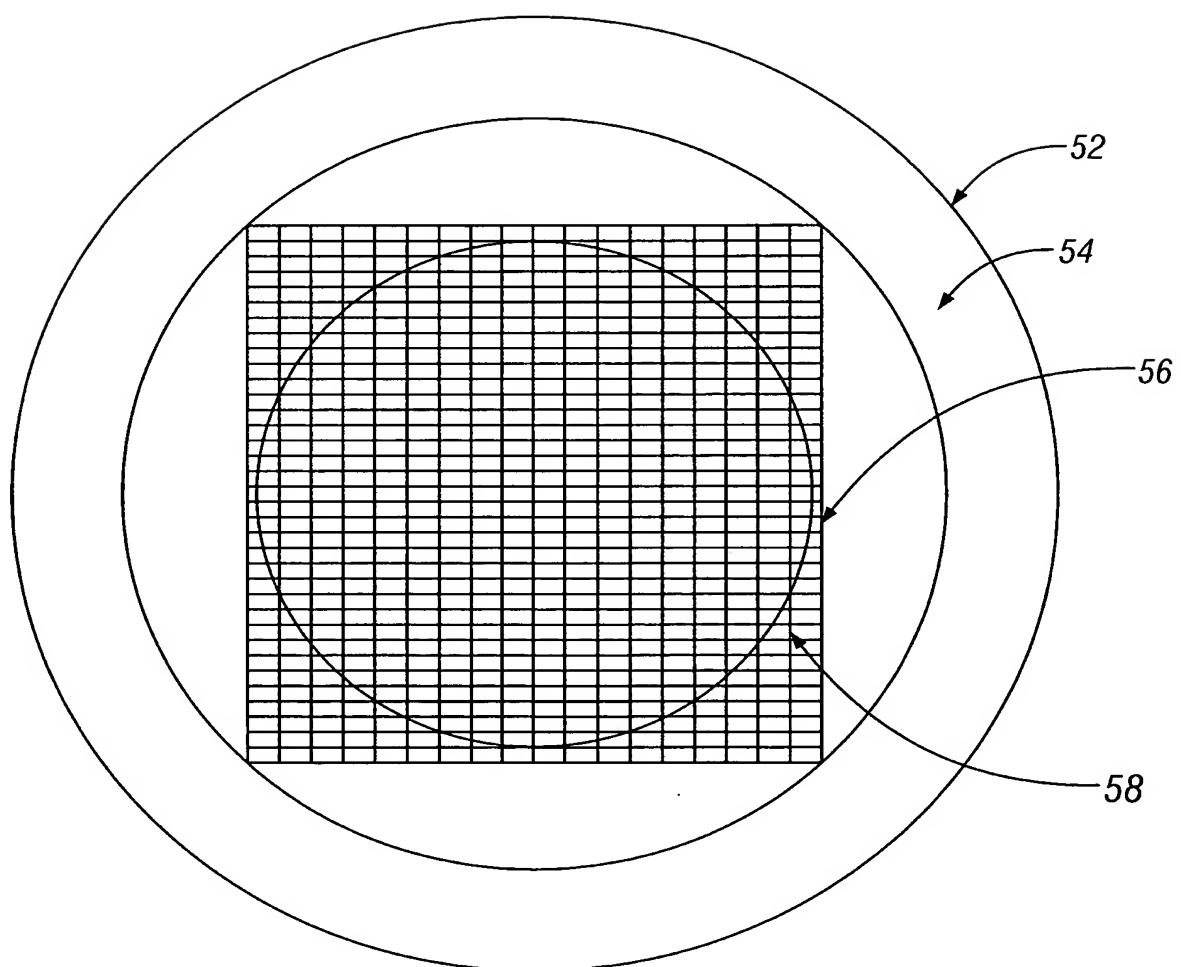


FIG. 5

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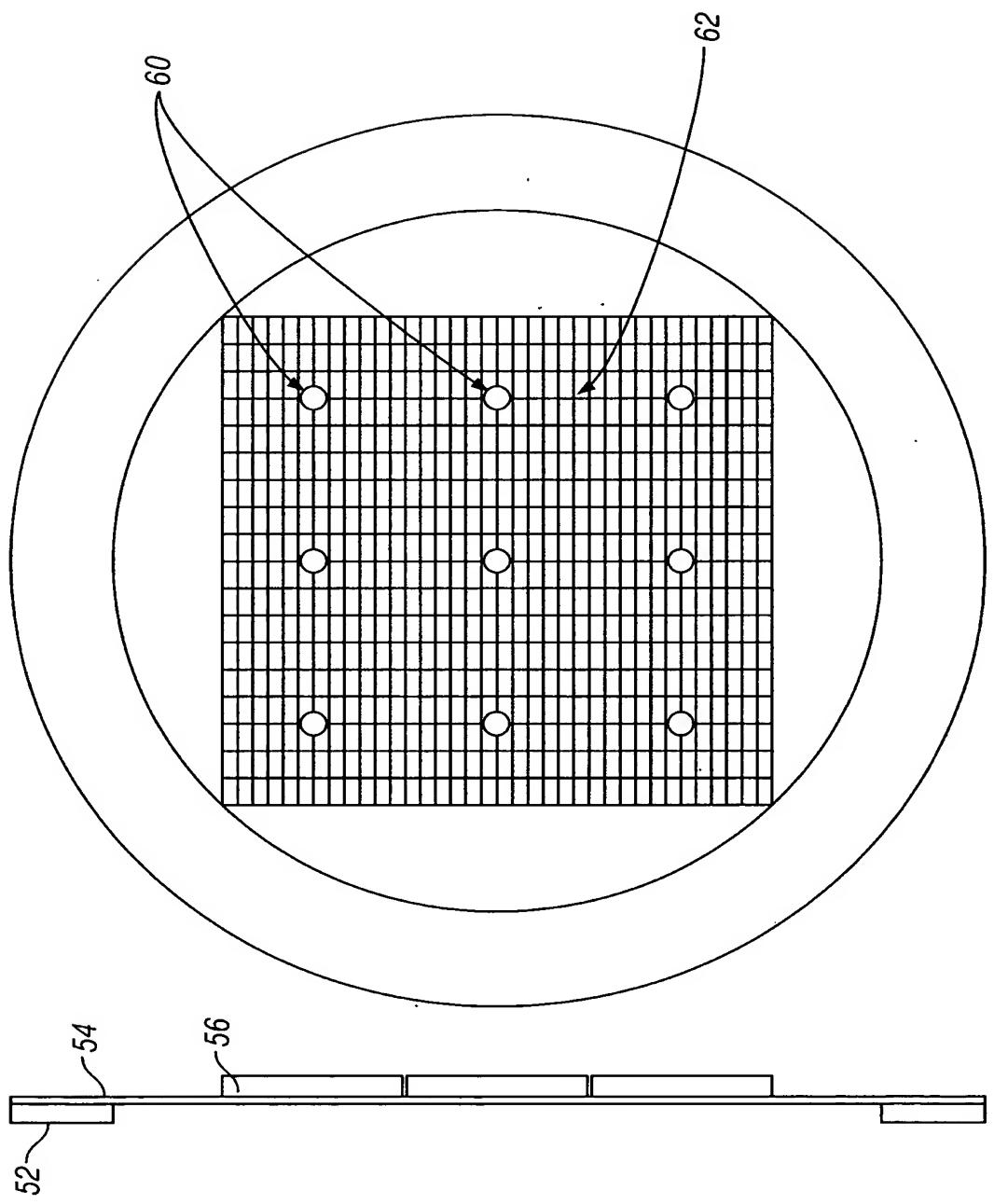


FIG. 6A

FIG. 6B

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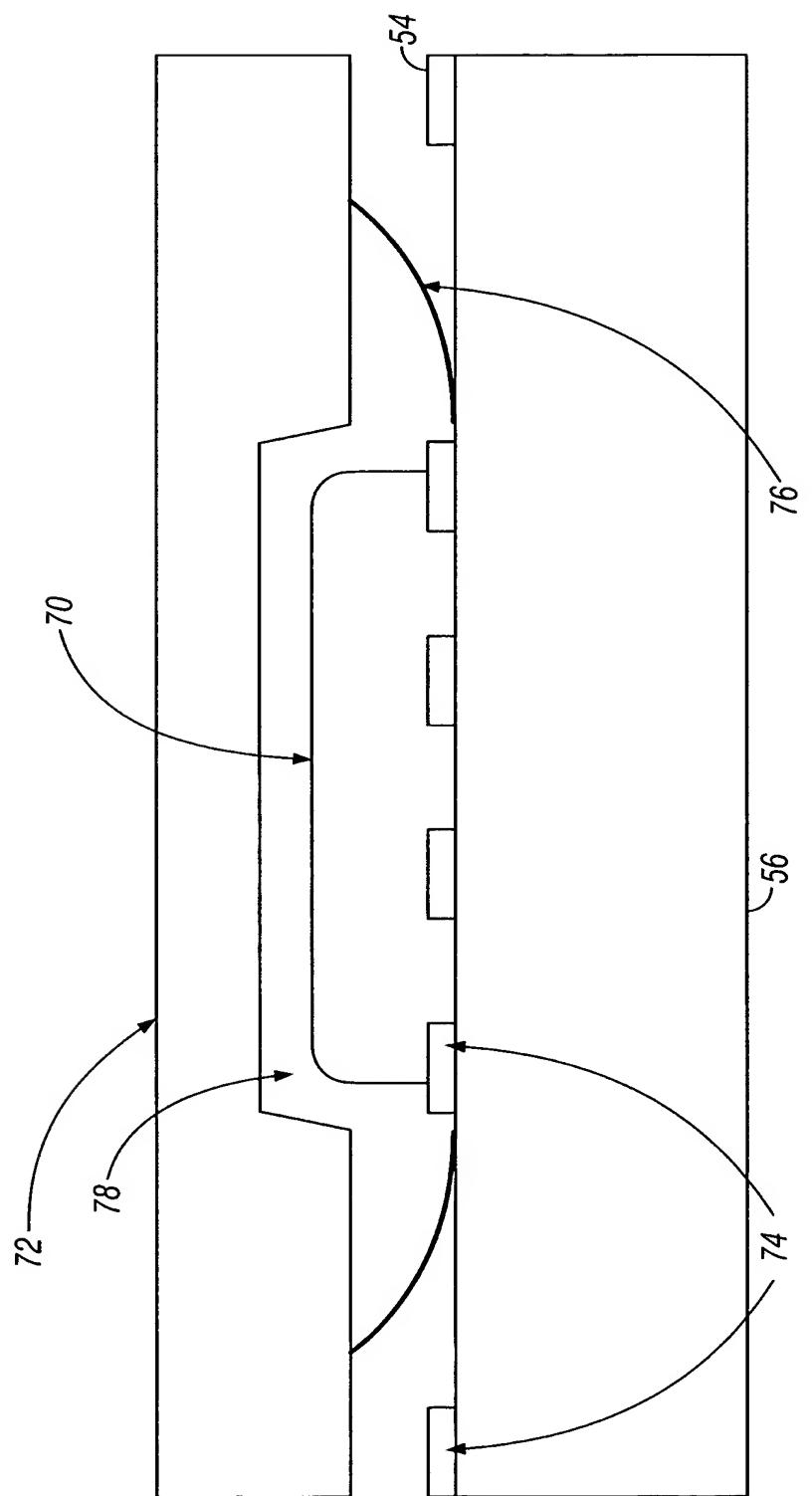


FIG. 7

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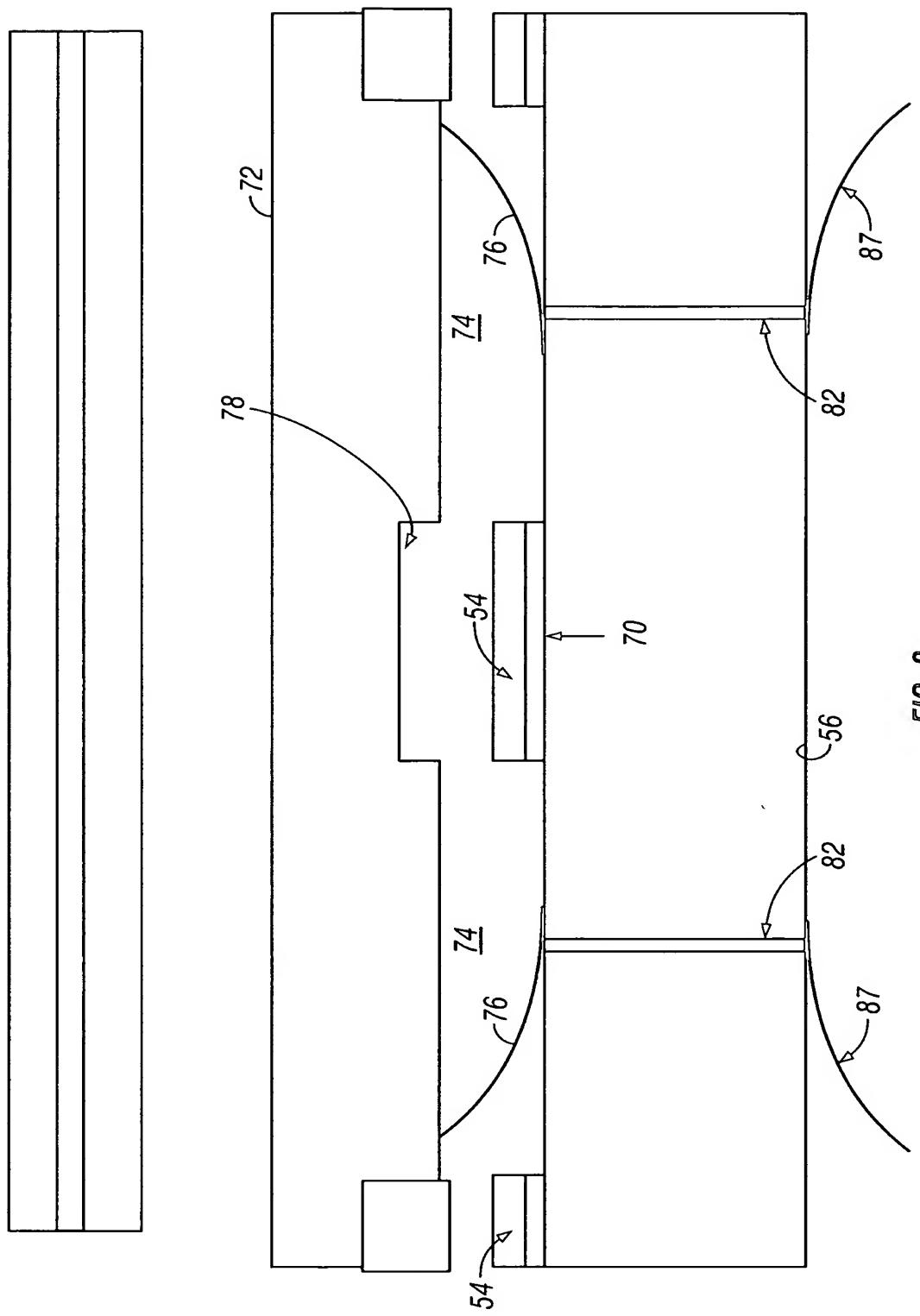


FIG. 8

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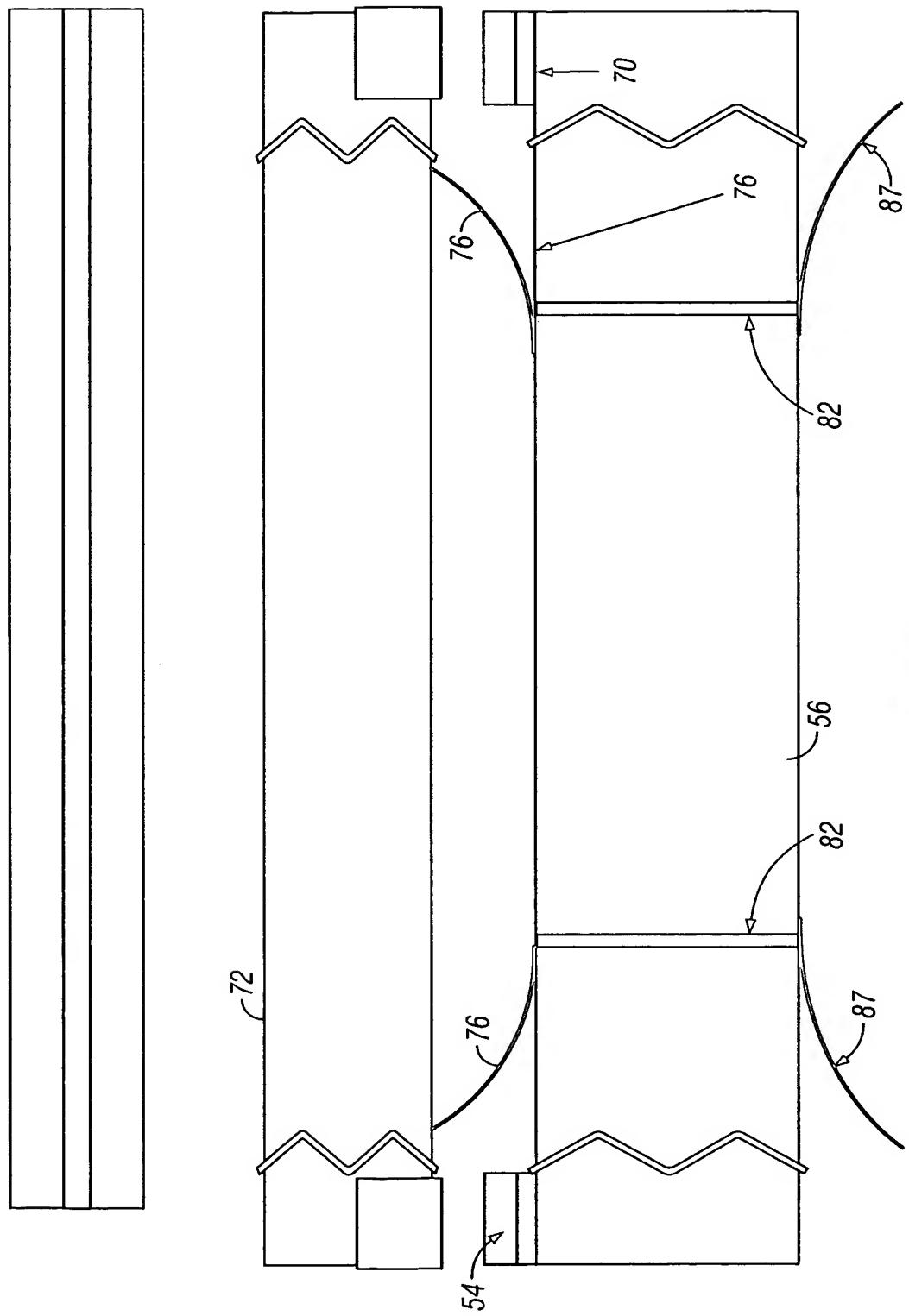


FIG. 9